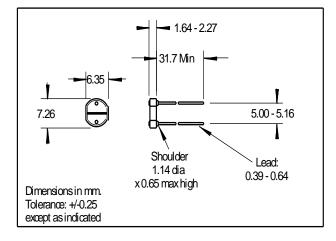


# TO-5 Ceramic Photocell

# **NSL-5532**



### **FEATURES**

- Passive resistance output
- Ceramic package

#### DESCRIPTION The NSL-5532 is a CdS photoconductive cell on a TO-5 ceramic

substrate. The photocell is encapsulated with epoxy for moisture

## **APPLICATIONS**

Industrial

## ABSOLUTE MAXIMUM RATING

resistance.

(TA)= 23°C UNLESS OTHERWISE NOTED

SYMBOL	PARAMETER	MIN	MAX	UNITS
VP	Voltage (peak AC or DC)		320	V
P <sub>d</sub>	Power Dissipation @ 25°C (1)		125	mW
T <sub>Op</sub>	Operating Temperature	-60	+75	°C
T <sub>Stg</sub>	Storage Temperature	-60	+75	°C
Ts	Soldering Temperature (2)		+260	°C

#### Note:

(1) Derate linearly to 0 at 75°C

- (2) >0.08" from case for <5 sec.
- (3) Cells light adapted at 30 to 50 Ftc for 16 hrs minimum prior to electrical tests.

(TA)= 23°C, UNLESS OTHERWISE NOTED

#### RELIABILITY

Contact API for recommendations on specific test conditions and procedures.

# ELECTRO-OPTICAL CHARACTERISTICS

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	MIN	TYP	MAX	UNITS
$R_L$	Light Resistance	2 ftc., 2854°K (3)	110	165	220	KΩ
		100 ftc., 2854°K		10		KΩ
R <sub>D</sub>	Dark Resistance	5 sec after removal of test light.	11			MΩ
λ <sub>P</sub>	Spectral Peak			550		nm

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